

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Koji SAKUI

SERIAL NO: New Application

GAU:

FILED: Herewith

EXAMINER:

FOR: NONVOLATILE SEMICONDUCTOR MEMORY

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☒ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

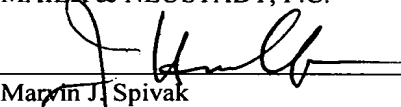
- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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LIST OF RELATED CASES

<u>Docket Number</u>	<u>Serial or Patent Number</u>	<u>Filing or Issue Date</u>	<u>Inventor/ Applicant</u>
PER CLIENT	6,307,807	10/23/01	SAKUI et al.
PER CLIENT	6,370,081	04/09/02	SAKUI et al.
PER CLIENT	6,512,703	01/28/03	SAKUI et al.

DOCKET NO.: 250849US2S

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STATEMENT OF RELEVANCY

Reference AO (11-195718) on Form PTO-1449:

This publication is referred to in the specification. See page 11, line 22.

Reference AP (11-297081) on Form PTO-1449:

This publication is referred to in the specification. See page 11, line 24.

Reference AQ (2000-149581) on Form PTO-1449:

This publication is referred to in the specification. See page 11, line 26.

Reference AR (2002-43444) on Form PTO-1449:

This publication is referred to in the specification. See page 12, line 1.

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 250849US2S		SERIAL NO. New Application	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Koji SAKUI			
				FILING DATE Herewith		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	5,777,925	07/07/98	Kaoru TOKUSHIGE			
	AB	4,636,984	01/13/87	Hans R. NEUKOMM			
	AC	6,307,807	10/23/01	Koji SAKUI, et al.			
	AD	6,370,081	04/09/02	Koji SAKUI, et al.			
	AE	6,512,703	01/28/03	Koji SAKUI, et al.			
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO	11-195718	07/21/99	Japan			X
	AP	11-297081	10/29/99	Japan			X
	AQ	2000-149581	05/30/00	Japan			X
	AR	2002-43444	02/08/02	Japan			X
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	Art LANCASTER, et al. "A 5V-Only EEPROM with Internal Program/Erase Control", ISSCC DIGEST OF TECHNICAL PAPERS, 1983 IEEE International Solid-State Circuits Conference, Session XIII: NONVOLATILE MEMORY, February 24, 1983, Page 164-165 and Page 302					
	AX	Fujio MASUOKA, et al., "New Ultra High Density EPROM and Flash EEPROM with NAND Structure Cell", IEDM 87, Pages 552-555					
	AY	Fujio MASUOKA, et al., "A New Flash E ² PROM Cell Using Triple Polysilicon Technology", IEDM 84, Pages 464-467					
	AZ	William S. JOHNSON, et al. "A 16Kb Electrically Erasable Nonvolatile Memory" DIGEST OF TECHNICAL PAPERS, 1980 IEEE International Solid-State Circuits Conference, Session XII: ROMs, PROMs and EROMs, February 14, 1980, Page 152-153 and Page 271				<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							